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You are hereby authorized to correct the drawing(s) in the case indicated below:

- ☒ Correct informalities cited by Chief Draftsman (Include copy of Form #948)
- ☒ Enter changes approved by the Examiner (Include prints with changes marked in red)
- ☒ When double-line hatching or criss-cross hatching cited - advise correct hatching.

Special Instructions:

- ☐ Forward the formal drawings and file copies.
- OR
- ☒ Forward a copy of changed drawings and notification of date refiled in USPTO to address below.
- ☒ A copy of the drawings as presently on file are enclosed for your use in making the required corrections.

ATTACH THE NOTICE OF ALLOWABILITY OR NOTICE OF ALLOWANCE

Name: NIXON PEABODY  
Address: 8180 Greensboro Drive, Suite 800  
McLean, Virginia 22102  
Phone No. (703) 790-9110

Docket: 0756-1838

Date: 12/14/2001

Corrected drawings due in USPTO no later than 02/05/2002

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NEW DRAWINGS


Please allow Quinn Patent Drawing Service to file the attached new drawing sheets for the ones originally filed.

Applicant Shunpei YAMAZAKI; Satoshi TERAMOTO, and Yasuyuki ARAI

Serial No. 09/118,010 Filing Date 07/17/1998

Title: Semiconductor Device, Method of Fabricating Same, and Electrooptical Device

Date Allowed 11/05/2001 Batch No. O35

  
Eric J. Robinson  
Serial No. 38,285

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11.01

09/11/2010  
09118060

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FIG. 1(A)

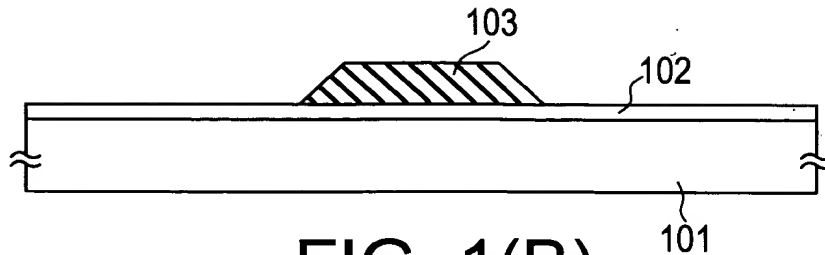


FIG. 1(B)

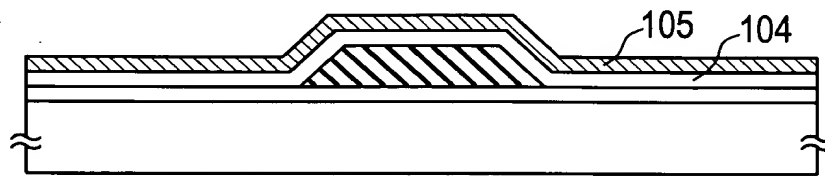


FIG. 1(C)

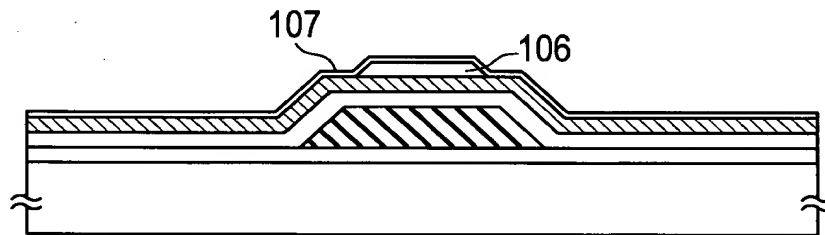


FIG. 1(D)

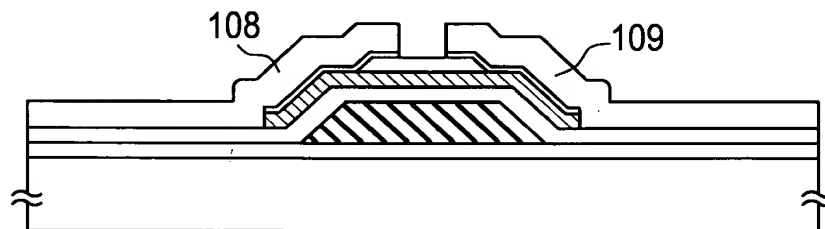


FIG. 1(E)

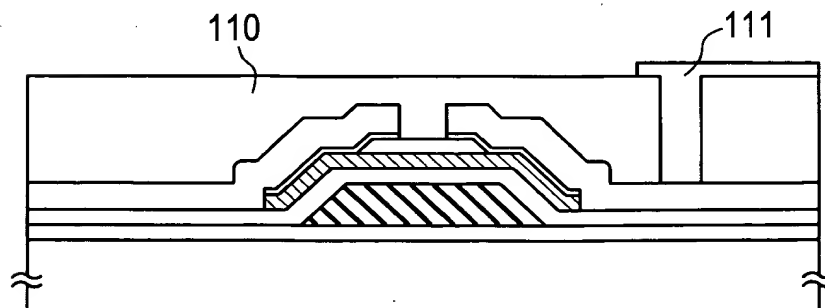


FIG. 2(A)

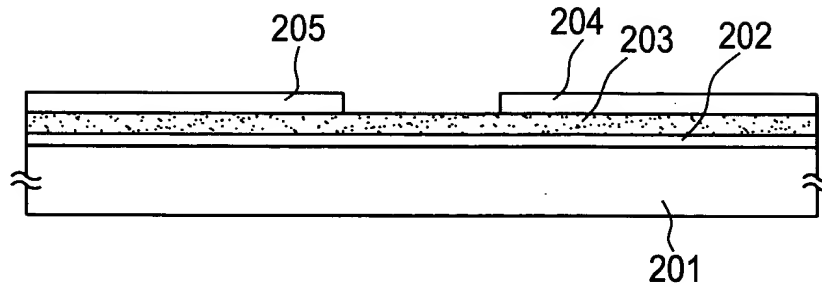


FIG. 2(B)

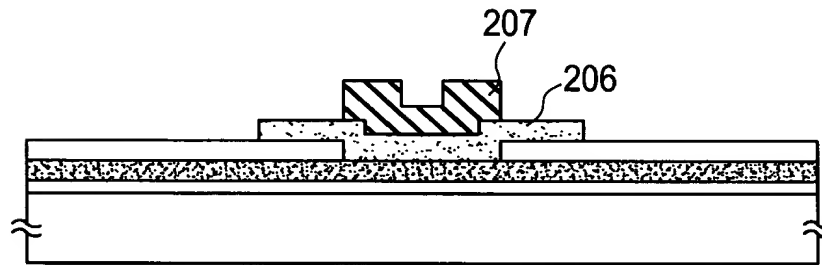
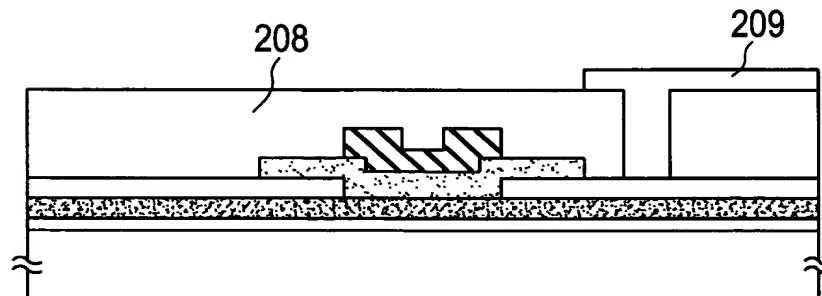


FIG. 2(C)



APPROVED	O.G. FIG. 3	
BY	CLASS	SUBCLASS
DRAFTSMAN	257	59

09/118,010

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FIG. (3)

